



PATENT
2450-0570P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: HWANG, Jyn-Guo et al. Conf.:
Appl. No.: 10/687,877 Group:
Filed: October 20, 2003 Examiner:
For: CMOS IMAGE SENSOR SINGLE CHIP INTEGRATED
WITH A RF TRANSMITTER

ASSERTION OF SMALL ENTITY STATUS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 3, 2003

Sir:

The above-identified application qualifies for small entity status. This written assertion of small entity status should satisfy the requirements of 37 C.F.R. § 1.27.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

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2450-0570

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Attachment(s)